

Device for detecting the temperature of an oscillator crystal

The invention relates to a device for detecting the temperature of an oscillator crystal that has a crystal vibrator in an oscillator-crystal housing, in particular in a mobile radio apparatus.

5 The textbook: Neubig, Briese; "*Das große Quarzkochbuch*" [The big crystal cookbook], Franzis-Verlag, p. 51 to 54 describes how, in order to measure temperatures, the temperature dependency of the frequency of oscillator crystals can be used. The oscillator crystal serves *per se* for the temperature measurement. It is not provided in order to supply a particular operating frequency in an apparatus, such as a mobile radio apparatus or equipment used in entertainment electronics, automotive technology or medicine.

10 Described in JP 2001-077627 is a temperature-compensated, piezoelectric oscillator. In order to achieve a small size, a temperature compensation circuit with a thermistor in thick-film technology is applied to the rear wall of the housing of the oscillator circuit for direct analog temperature compensation. The thermistor is decoupled from the oscillator by the rear wall in respect of the temperature acting upon it. The temperature and
15 the temperature gradient at the thermistor and at the frequency-determining element of the oscillator here deviate from each other to a greater or lesser extent.

Described in US 4 862 110 is a Surface Acoustic Wave (SAW) resonator, which is tuned to its nominal frequency by temperature variation of the resonator. A regulated heating element is provided for this purpose.

20 It is an object of the invention to propose a device of the kind specified above in which the measured temperature is as exact as possible a replica of the temperature to which the oscillator crystal, or its crystal vibrator as the frequency-determining component, is subjected.

25 This object is achieved by means of the features as claimed in claim 1. Since, with this device, the detection of the temperature and the temperature gradient takes place on the oscillator crystal directly in terms of both time and space, it is precisely the temperature influencing the frequency response of the oscillator crystal that is detected by means of the temperature sensor. By virtue of this direct detection, no delays, inertia or deformations of the impulse response of the temperature propagation between the oscillator crystal and the

temperature sensor occur. A precise compensation of the frequency error of the oscillator circuit of the oscillator crystal respectively occurring as a function of the temperature is thereby made possible. A software temperature compensation may take place by means of targeted influencing of the actuators serving for frequency correction.

5 The electrical parallel connection of the temperature sensor is advantageous because, as a result, the oscillator crystal with temperature sensor is a solely two-terminal component, which can be accommodated on a printed circuit board with minimal wiring requirement. The device permits a minimization of the number of components necessary for the temperature detection. These can be integrated into an integrated circuit. Because of the
10 omission of external components, an inexpensive structure with a small space requirement and low susceptibility to error is guaranteed.

 In a mobile radio apparatus, the device described solves the temperature detection problems that arise as a result of the fact that temperature gradients with different signs propagate in the mobile radio apparatus, wherein, on the one hand, self-heating occurs
15 through energy loss and, on the other, heating or cooling occurs through the environment. The device described may also be used with other equipment, e.g. equipment used in entertainment electronics, medical engineering or automotive engineering.

 In an embodiment of the invention, the temperature sensor is applied to a constant-current source or a constant-voltage source and an evaluation circuit which
20 evaluates the temperature and/or the temperature gradients for compensation of the temperature-dependent resonant frequency of the oscillator crystal.

 The invention will be further described with reference to examples of
25 embodiments shown in the drawings, to which, however, the invention is not restricted.

 Fig. 1 shows a plan view of a temperature-dependent resistor (thermistor) in isothermal arrangement to an oscillator crystal on a printed circuit board.

 Fig. 2 shows, as an alternative to Fig. 1, a schematic sectional view of a thermistor inside an oscillator crystal housing.

30 Fig. 3 shows the electrical parallel connection of an oscillator crystal or crystal vibrator and a thermistor.

 Fig. 4 shows a parallel connection of a thermistor with a series combination of an oscillator crystal or crystal vibrator and coupling capacitors.

Fig. 5 shows a block circuit diagram of an evaluation circuit and an oscillator circuit with a constant-current source and the parallel connection as shown in Fig. 3.

Fig. 6 shows a further version of the evaluation circuit with a constant-voltage source.

5 Fig. 7 shows a further version of the evaluation circuit with a constant-current source with sequential temperature measurement.

Fig. 8 shows a detailed representation of a phase-locked loop.

10 In figure 1, a printed circuit board 1 of an apparatus, e.g. a mobile radio apparatus or an apparatus used in entertainment electronics, automotive technology or medical engineering, is provided as the carrier of a frequency-determining circuit, which is equipped with an oscillator crystal. Arranged on the printed circuit board 1 are an oscillator crystal 2 and an integrated circuit 3. The oscillator crystal 2 is equipped with an oscillator-
15 crystal housing 2', in which is a crystal vibrator 4 (see figure 2), which is connected via printed lines 5, 6 of the printed circuit board 1 to terminals A and B of the integrated circuit 3.

Arranged on the printed circuit board 1 as a temperature sensor is a temperature-dependent resistor, especially the thermistor 7, which is electrically connected to
20 the printed lines 5, 6 in parallel with the crystal vibrator 4. The thermistor 7 is arranged isothermally in relation to the oscillator-crystal housing 2' in the area 52. In particular, in the vicinity 50 of the oscillator-crystal housing 2', the thermistor 7 is arranged on the same side of the printed circuit board 1 as the oscillator-crystal housing 2' and not separated from it by a wall. It is thereby achieved that the temperature and the temperature gradient at the
25 oscillator crystal 2, specifically at its crystal vibrator 4, and at the thermistor 7 are in essence identical. An additional thermal conduction means, e.g. heat transfer compound, may support the immediate vicinity 50 of the temperature sensor 7 at the oscillator-crystal housing 2'.

In order to increase the heat transfer resistance between the heat-emitting circuit 3 and the oscillator crystal 2, and thereby to support the isothermal arrangement in the
30 area 52, openings 51 in the printed circuit board material may be provided in the printed circuit board 1.

It is then obvious that, as the temperature sensor, the thermistor 7 is subjected to the particular temperature and temperature gradient that have a frequency-determining effect on the crystal vibrator 4. The integrated circuit 3 contains the evaluation circuit that is

further described below. It is spaced at a distance from the thermistor 7, so that its temperature scarcely affects the thermistor 7.

Figure 2 shows an oscillator crystal 2 in Surface-Mounted Device (SMD) design, wherein the thermistor 7 is arranged not, as in figure 1, adjacent to, but inside the oscillator-crystal housing 2'. The oscillator-crystal housing 2' of ceramic, metal or plastic, forms an inner chamber 8 filled with inert gas. Inside this, the crystal vibrator 4 is located on mountings 9. The crystal vibrator 4 is connected to oscillator-crystal terminals 10, which lead outwards. A base panel 11 of the oscillator-crystal housing 2' is suitable to act as a carrier for the attachment of components and lines, and takes the form of, for example, a printed circuit board.

The thermistor 7 is integrated into, or applied to, the base panel 11 serving as the carrier. It is located inside the inner chamber 8, as near as possible to the crystal vibrator 4, but must not touch the latter, since the oscillation properties could be negatively influenced as a result. The thermistor 7 is not separated from the crystal vibrator 4 by a wall, and is provided on the same side of the base panel 11 as the crystal vibrator 4. The thermistor 7 is connected by connecting leads 12 to the crystal terminals 10 in such a way that the crystal vibrator 4 and the thermistor 7 are electrically connected parallel to the oscillator-crystal terminals 10.

Figure 3 shows the parallel connection of the oscillator crystal 2, or the crystal vibrator 4, and to the thermistor 7. Given the typical values of a crystal-oscillator circuit, the thermistor 7 does not represent any significant additional load on the oscillator crystal. The thermistor 7 has, for example, a nominal ohmic resistance of approximately 30 k Ω .

In the case of the alternative shown in figure 4, coupling capacitors Ck are connected in series to the oscillator crystal 2, specifically the crystal vibrator 4. The thermistor 7 is connected parallel to this series connection. This layout too may be integrated into the oscillator-crystal housing 2' in the case of an arrangement as shown in figure 2. The coupling capacitors Ck are then arranged on the base panel 11.

In the case of a layout as shown in figure 1, this arrangement is applied to the isothermal area 52 of the printed circuit board 1.

In their operational mode, the coupling capacitors Ck separate the direct voltage to be applied to the thermistor 7, which is further described below, from the oscillator crystal 2, or the oscillator vibrator 4.

Figures 5, 6 and 7 show evaluation circuits and oscillator circuits that may be integrated into the integrated circuit 3. With the evaluation circuits, the temperature detected

at the thermistor 7 can be evaluated in such a way that, as the end result, the temperature response of the resonant frequency of the oscillator crystal 2 is compensated. The thermistor 7 has a known resistance/temperature characteristic.

5 A constant current source 13 (see figure 5) impresses a constant current on the thermistor 7. As a result, a direct voltage, which corresponds to the present temperature-dependent resistance value of the thermistor 7, arises between the terminals A, B. This direct voltage is detected by an analog/digital converter 14, and sent digitally, via a data processing lead 61, to a microcontroller 15 of the apparatus. This microcontroller 15 determines the real-time temperature, e.g. from a voltage/temperature Table stored within it, which corresponds to the characteristic curve of the thermistor 7. The microcontroller 15 determines the voltage
10 gradient or temperature gradient from sequential measurements.

An amplifier 16 serves to excite and maintain the high-frequency oscillations of the oscillator equipped with the oscillator crystal 2, which, as a Pierce oscillator, is equipped with capacitors 17 from terminal A and from terminal B to ground. The oscillator
15 crystal 2, especially its crystal vibrator 4, with the amplifier 16 and the capacitors 17 forms an oscillator circuit. The capacitors 17 are preferably integrated into the integrated circuit 3. In order to provide the facility of frequency control, the capacitors 17 may be adjustable in terms of their capacitance value. For the possibility of adjustment, the capacitors 17 are connected to the microcontroller 15 via control leads 60. The HF oscillations, e.g. 26 MHz,
20 of the oscillator are passed on via a lead 34 from the terminal A to a phase-locked loop 18 of the apparatus.

In order that the operation of the amplifier 16 is not impaired by the direct-voltage path of the thermistor 7, coupling capacitors 19 are provided at the output and/or at the input of the amplifier 16.

25 In addition to the measurement direct voltage, the high-frequency signal voltage of the oscillator function is also present at the terminals A, B, and therefore also at the analog/digital converter 14. The high-frequency signal voltage is eliminated by signal processing measures, e.g. a low-pass filtration, in the analog/digital converter 14 or in the microcontroller 15, so that only the direct-voltage measurement signal is used for the further
30 processing in the microcontroller.

In a further embodiment, the determination of the temperature or temperature change from the direct-voltage measurement may be assigned to the analog/digital converter 14 itself if it can have direct recourse to the characteristic curve of the thermistor 7. The

microcontroller 15 can then signal, via the lead 34, that large temperature gradients are currently occurring.

In the case of the evaluation circuit as shown in figure 6, a constant-voltage source 20 is provided instead of the constant-current source. In addition, a resistor 21 is integrated into the integrated circuit 3. Together with the thermistor 7, the resistor 21 forms a voltage divider, so that, here again, a direct voltage that is dependent on the temperature arises between the terminals A and B.

In the case of the embodiments as shown in figures 5 and 6, the direct-voltage measurement serving for temperature detection and the oscillator operation take place simultaneously. Conversely, in the embodiment as shown in figure 7, the direct-voltage measurement and the oscillator operation take place sequentially in cycles. An enable-signal lead 22, controlled by the microcontroller, is provided for this purpose. Either the constant-current source 13 and the analog/digital converter 14 are switched on via this, or the amplifier 16 is switched on via an inverter 23 for the enable signal. In this way, the temperature can be detected before the operation of the oscillator circuit, and, if applicable, a temperature compensation of the oscillator frequency or a temperature calibration can be undertaken.

With this layout, the coupling capacitors 19 (see figures 5 and 6) and the coupling capacitors Ck (see figure 4) are superfluous. For the sake of clarity, the control leads 60 and 61 are omitted in figure 7, although they are used as shown in figures 5 and 6.

In another embodiment, the analog/digital converter 14 may be arranged with spatial separation from the microcontroller 15. If the resistance/temperature characteristic curve of the thermistor 7 is known to it, the analog/digital converter 14 can detect that temperature gradients presently occurring exceed a previously-defined or programmable limit value. The analog/digital converter 14 can then signal this to the microcontroller 15 via a control lead.

Figures 5 to 7 show the case where no coupling capacitors Ck are provided in the isothermal area 52. The evaluation circuits of figures 5 to 7 may, however, also be used if coupling capacitors Ck are arranged in the isothermal area 52 (see figure 4). The evaluation circuits in figures 5 to 7 may also be used if, as shown in figure the thermistor 7 is arranged inside the oscillator-crystal housing 2' with or without coupling capacitors Ck.

Figure shows the principle of the phase-control loop 18 (see figures 5, 6 and 7). It derives the output frequency occurring at an output 30 of a voltage-controlled oscillator 31 from the reference frequency occurring on the lead 34, i.e. at terminal A, with phase-locking and frequency-locking. To develop the phase-locked loop, a divider 35, a phase

comparator 33 and a loop filter 32 are provided adjacent to the voltage-controlled oscillator 31.

5 The divider 35 can set fractional-rational division ratios in virtually any degree of fineness-stepping. It is a known fractional N divider. In the alternative as shown in figure 8, there is a special feature in that the reference frequency originating from the crystal oscillator circuit (see figure 5) on the lead 34 is not, as described above, tracked to its nominal value by variation of the capacitances of the capacitors 17. Instead, the reference frequency maintains its temperature-dependent frequency departure and it is achieved, through an appropriate, finely-stepped reprogramming of the divider 35, that the frequency at 10 the output 30 exhibits the nominal frequency value. The reprogramming of the divider 35 takes place via a data lead (not shown) from the microcontroller 15. The temperature information present in the microcontroller 15 hereby brings about a temperature compensation of the output frequency at the output 30.

15 The frequency tracking, i.e. temperature compensation, can therefore take place either via a readjustment of the capacitors 17 or, alternatively, via an appropriate reprogramming of the divider 35.

The temperature information obtained may also be used for other purposes. For example, it may additionally be used in a mobile radio apparatus for calibrating other temperature-dependent parameters or for safety shutdown when a battery is charged.